Lecture #03 – Representing and Operating on Integers
Plan For Today

- Signed Integers
- Overflow
- Casting and Combining Types

Disclaimer: Slides for this lecture were borrowed from

—Nick Troccoli's Stanford CS107 class
Recap: Unsigned Integers

• An **unsigned** integer is 0 or a positive integer (no negatives).

• Converting between decimal and binary has a nice 1:1 relationship.

• The range of an unsigned number is $0 \rightarrow 2^w - 1$, where $w$ is the number of bits. E.g. a 32-bit integer can represent 0 to $2^{32} - 1$ (4,294,967,295).
Lecture Plan

• Signed Integers
• Overflow
• Casting and Combining Types
Signed Integers

- A **signed** integer is a negative integer, 0, or a positive integer.
- *Problem*: How can we represent negative *and* positive numbers in binary?
Signed Integers

- A **signed** integer is a negative integer, 0, or a positive integer.
- *Problem:* How can we represent negative *and* positive numbers in binary?

**Idea:** let’s reserve the *most significant bit* to store the sign.
Sign Magnitude Representation

0110
positive  6

1011
negative  3
Sign Magnitude Representation

0000

positive 0

1000

negative 0
### Sign Magnitude Representation

<p>| | | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1 000 = -0</td>
<td>0 000 = 0</td>
<td></td>
</tr>
<tr>
<td>1 001 = -1</td>
<td>0 001 = 1</td>
<td></td>
</tr>
<tr>
<td>1 010 = -2</td>
<td>0 010 = 2</td>
<td></td>
</tr>
<tr>
<td>1 011 = -3</td>
<td>0 011 = 3</td>
<td></td>
</tr>
<tr>
<td>1 100 = -4</td>
<td>0 100 = 4</td>
<td></td>
</tr>
<tr>
<td>1 101 = -5</td>
<td>0 101 = 5</td>
<td></td>
</tr>
<tr>
<td>1 110 = -6</td>
<td>0 110 = 6</td>
<td></td>
</tr>
<tr>
<td>1 111 = -7</td>
<td>0 111 = 7</td>
<td></td>
</tr>
</tbody>
</table>

- We’ve only represented 15 of our 16 available numbers!
Sign Magnitude Representation

• **Pro:** easy to represent, and easy to convert to/from decimal.
• **Con:** +0 is not intuitive
• **Con:** we lose a bit that could be used to store more numbers
• **Con:** arithmetic is tricky: we need to find the sign, then maybe subtract (borrow and carry, etc.), then maybe change the sign. This complicates the hardware support for something as fundamental as addition.

Can we do better?
A Better Idea

- Ideally, binary addition would *just work regardless* of whether the number is positive or negative.
A Better Idea

• Ideally, binary addition would *just work regardless* of whether the number is positive or negative.

\[
\begin{align*}
0101 & \quad +1011 \\
\hline
0000 & \\
\end{align*}
\]
A Better Idea

• Ideally, binary addition would *just work regardless* of whether the number is positive or negative.

```
  0011
+????
  0000
```
A Better Idea

- Ideally, binary addition would *just work regardless* of whether the number is positive or negative.

\[
\begin{array}{c}
0011 \\
+1101 \\
\hline
0000
\end{array}
\]
A Better Idea

• Ideally, binary addition would *just work regardless* of whether the number is positive or negative.

```
  00000
+?????
  00000
```
A Better Idea

- Ideally, binary addition would just work regardless of whether the number is positive or negative.

```
  00000
+00000
-----
  00000
```
<table>
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<tr>
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<th>Positive</th>
<th>Negative</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0000</td>
<td>0000</td>
</tr>
<tr>
<td>1</td>
<td>0001</td>
<td>1111</td>
</tr>
<tr>
<td>2</td>
<td>0010</td>
<td>1110</td>
</tr>
<tr>
<td>3</td>
<td>0011</td>
<td>1101</td>
</tr>
<tr>
<td>4</td>
<td>0100</td>
<td>1100</td>
</tr>
<tr>
<td>5</td>
<td>0101</td>
<td>1011</td>
</tr>
<tr>
<td>6</td>
<td>0110</td>
<td>1010</td>
</tr>
<tr>
<td>7</td>
<td>0111</td>
<td>1001</td>
</tr>
</tbody>
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<th>Negative</th>
</tr>
</thead>
<tbody>
<tr>
<td>8</td>
<td>1000</td>
<td>1000</td>
</tr>
<tr>
<td>9</td>
<td>1001 (same as -7!)</td>
<td>NA</td>
</tr>
<tr>
<td>10</td>
<td>1010 (same as -6!)</td>
<td>NA</td>
</tr>
<tr>
<td>11</td>
<td>1011 (same as -5!)</td>
<td>NA</td>
</tr>
<tr>
<td>12</td>
<td>1100 (same as -4!)</td>
<td>NA</td>
</tr>
<tr>
<td>13</td>
<td>1101 (same as -3!)</td>
<td>NA</td>
</tr>
<tr>
<td>14</td>
<td>1110 (same as -2!)</td>
<td>NA</td>
</tr>
<tr>
<td>15</td>
<td>1111 (same as -1!)</td>
<td>NA</td>
</tr>
</tbody>
</table>
There Seems Like a Pattern Here…

- The negative number is the positive number inverted, plus one!
There Seems Like a Pattern Here…

A binary number plus its inverse is all 1s.

\[
\begin{align*}
0101 & + 1010 \\
& \underline{+ 0001} \\
& 1111
\end{align*}
\]

Add 1 to this to carry over all 1s and get 0!

\[
\begin{align*}
1111 & + 0001 \\
& \underline{+ 0000} \\
& 0000
\end{align*}
\]
Another Trick

• To find the negative equivalent of a number, work right-to-left and write down all digits *through* when you reach a 1. Then, invert the rest of the digits.

\[100100 + ???????? = 0000000\]
Another Trick

• To find the negative equivalent of a number, work right-to-left and write down all digits *through* when you reach a 1. Then, invert the rest of the digits.

\[
\begin{array}{c}
100100 \\
+ \text{ ???100} \\
\hline
0000000
\end{array}
\]
Another Trick

• To find the negative equivalent of a number, work right-to-left and write down all digits *through* when you reach a 1. Then, invert the rest of the digits.

\[
\begin{align*}
100100 & \\
+ & 011100 \\
\hline
0000000 & \\
\end{align*}
\]
Two’s Complement

4-bit two's complement signed integer representation
Two’s Complement

• In two’s complement, we represent a positive number as itself, and its negative equivalent as the two’s complement of itself.

• The two’s complement of a number is the binary digits inverted, plus 1.

• This works to convert from positive to negative, and back from negative to positive!
Two’s Complement

- **Con**: more difficult to represent, and difficult to convert to/from decimal and between positive and negative.
- **Pro**: only 1 representation for 0!
- **Pro**: all bits are used to represent as many numbers as possible
- **Pro**: the most significant bit still indicates the sign of a number.
- **Pro**: addition works for any combination of positive and negative!
Two’s Complement

• Adding two numbers is just...adding! There is no special case needed for negatives. E.g. what is 2 + -5?

\[
\begin{array}{r}
0010 & \quad 2 \\
+1011 & \quad -5 \\
\hline
1101 & \quad -3
\end{array}
\]
Two’s Complement

- Subtracting two numbers is just performing the two’s complement on one of them and then adding. E.g. $4 - 5 = -1$. 

```
0100  4
-0101  5
-----
0100  4
+1011  -5
-----
1111  -1
```
Practice: Two’s Complement

What are the negative or positive equivalents of the numbers below?

a) -4 (1100)
b) 7 (0111)
c) 3 (0011)
d) -8 (1000)
Lecture Plan

• Signed Integers
• Overflow
• Casting and Combining Types
Overflow

• If you exceed the **maximum** value of your bit representation, you *wrap around* or *overflow* back to the **smallest** bit representation.

  \[0b1111 + 0b1 = 0b0000\]

• If you go below the **minimum** value of your bit representation, you *wrap around* or *overflow* back to the **largest** bit representation.

  \[0b0000 - 0b1 = 0b1111\]
# Min and Max Integer Values

<table>
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<tr>
<th>Type</th>
<th>Size (Bytes)</th>
<th>Minimum</th>
<th>Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>char</td>
<td>1</td>
<td>-128</td>
<td>127</td>
</tr>
<tr>
<td>unsigned char</td>
<td>1</td>
<td>0</td>
<td>255</td>
</tr>
<tr>
<td>short</td>
<td>2</td>
<td>-32768</td>
<td>32767</td>
</tr>
<tr>
<td>unsigned short</td>
<td>2</td>
<td>0</td>
<td>65535</td>
</tr>
<tr>
<td>int</td>
<td>4</td>
<td>-2147483648</td>
<td>2147483647</td>
</tr>
<tr>
<td>unsigned int</td>
<td>4</td>
<td>0</td>
<td>4294967295</td>
</tr>
<tr>
<td>long</td>
<td>8</td>
<td>-9223372036854775808</td>
<td>9223372036854775807</td>
</tr>
<tr>
<td>unsigned long</td>
<td>8</td>
<td>0</td>
<td>18446744073709551615</td>
</tr>
</tbody>
</table>
Min and Max Integer Values

INT_MIN, INT_MAX, UINT_MAX, LONG_MIN, LONG_MAX, ULONG_MAX, ...
Overflow
At which points can overflow occur for signed and unsigned int? (assume binary values shown are all 32 bits)

A. Signed and unsigned can both overflow at points X and Y
B. Signed can overflow only at X, unsigned only at Y
C. Signed can overflow only at Y, unsigned only at X
D. Signed can overflow at X and Y, unsigned only at X
E. Other
Unsigned Integers

≈ +4 billion  0

Discontinuity means overflow possible here

Increasing positive numbers

More increasing positive numbers
Signed Numbers

Discontinuity means overflow possible here

Increasing positive numbers

Negative numbers becoming less negative (i.e. increasing)

≈ -2billion

≈ +2billion
Overflow In Practice: PSY

YouTube: “We never thought a video would be watched in numbers greater than a 32-bit integer (=2,147,483,647 views), but that was before we met PSY. "Gangnam Style" has been viewed so many times we had to upgrade to a 64-bit integer (9,223,372,036,854,775,808)!"
Overflow In Practice: Timestamps

• Many systems store timestamps as the number of seconds since Jan. 1, 1970 in a signed 32-bit integer.

• Problem: the latest timestamp that can be represented this way is 3:14:07 UTC on Jan. 13 2038!
Overflow In Practice: Gandhi

• In the game “Civilization”, each civilization leader had an “aggression” rating. Gandhi was meant to be peaceful, and had a score of 1.

• If you adopted “democracy”, all players’ aggression reduced by 2. Gandhi’s went from 1 to 255!

• Gandhi then became a big fan of nuclear weapons.

https://kotaku.com/why-gandhi-is-such-an-asshole-in-civilization-1653818245
Overflow in Practice:

- Pacman Level 256
- Make sure to reboot Boeing Dreamliners every 248 days
- Comair/Delta airline had to cancel thousands of flights days before Christmas
- Reported vulnerability CVE-2019-3857 in libssh2 may allow a hacker to remotely execute code
- Donkey Kong Kill Screen
Demo Revisited: Unexpected Behavior

airline.c
Lecture Plan

• Signed Integers
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printf and Integers

• There are 3 placeholders for 32-bit integers that we can use:
  • %d: signed 32-bit int
  • %u: unsigned 32-bit int
  • %x: hex 32-bit int

• The placeholder—not the expression filling in the placeholder—dictates what gets printed!
Casting

• What happens at the byte level when we cast between variable types? The bytes remain the same! This means they may be interpreted differently depending on the type.

```c
int v = -12345;
unsigned int uv = v;
printf("v = %d, uv = %u\n", v, uv);
```

This prints out: "v = -12345, uv = 4294954951". Why?
Casting

• What happens at the byte level when we cast between variable types? The bytes remain the same! This means they may be interpreted differently depending on the type.

    int v = -12345;
    unsigned int uv = v;
    printf("v = %d, uv = %u\n", v, uv);

The bit representation for -12345 is 0b11111111111111001111100111.
If we treat this binary representation as a positive number, it's huge!
Casting

4-bit two's complement signed integer representation

4-bit unsigned integer representation
Comparisons Between Different Types

- **Be careful** when comparing signed and unsigned integers. C will implicitly cast the signed argument to unsigned, and then performs the operation assuming both numbers are non-negative.

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<td></td>
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<td></td>
</tr>
<tr>
<td><code>-2147483647 - 1</code></td>
<td></td>
<td><code>-1</code></td>
<td></td>
</tr>
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<td>Signed</td>
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<td>2147483647U &gt; -2147483647 - 1</td>
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Comparisons Between Different Types

- Be careful when comparing signed and unsigned integers. C will implicitly cast the signed argument to unsigned, and then performs the operation assuming both numbers are non-negative.

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Comparisons Between Different Types

Which many of the following statements are true? (assume that variables are set to values that place them in the spots shown)

- $s_3 > u_3$
- $u_2 > u_4$
- $s_2 > s_4$
- $s_1 > s_2$
- $u_1 > u_2$
- $s_1 > u_3$
Comparisons Between Different Types

Which many of the following statements are true? (assume that variables are set to values that place them in the spots shown)

- \( s_3 > u_3 \) - true
- \( u_2 > u_4 \)
- \( s_2 > s_4 \)
- \( s_1 > s_2 \)
- \( u_1 > u_2 \)
- \( s_1 > u_3 \)
Comparisons Between Different Types

Which many of the following statements are true? *(assume that variables are set to values that place them in the spots shown)*

s3 > u3 - true
u2 > u4 - true
s2 > s4
s1 > s2
u1 > u2
s1 > u3
Comparisons Between Different Types

Which many of the following statements are true? (assume that variables are set to values that place them in the spots shown)

- $s_3 > u_3$ - true
- $u_2 > u_4$ - true
- $s_2 > s_4$ - false
- $s_1 > s_2$
- $u_1 > u_2$
- $s_1 > u_3$
Comparisons Between Different Types

Which many of the following statements are true? (assume that variables are set to values that place them in the spots shown)

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\[ s_2 > s_4 \quad - \quad \text{false} \]
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- \( s_1 > s_2 \) - true
- \( u_1 > u_2 \) - true
- \( s_1 > u_3 \) - true
Expanding Bit Representations

- Sometimes, we want to convert between two integers of different sizes (e.g. `short` to `int`, or `int` to `long`).
- We might not be able to convert from a bigger data type to a smaller data type, but we do want to always be able to convert from a `smaller` data type to a `bigger` data type.
- For `unsigned` values, we can add *leading zeros* to the representation ("zero extension")
- For `signed` values, we can *repeat the sign of the value* for new digits ("sign extension")
- Note: when doing `<`, `>`, `<=`, `>=` comparison between different size types, it will *promote to the larger type*. 
Expanding Bit Representation

unsigned short s = 4;
// short is a 16-bit format, so
s = 0000 0000 0000 0100b

unsigned int i = s;
// conversion to 32-bit int, so i = 0000 0000 0000 0000 0000 0000 0000 0100b
Expanding Bit Representation

short s = 4;
// short is a 16-bit format, so  
s = 0000 0000 0000 0100b

int i = s;
// conversion to 32-bit int, so  
i = 0000 0000 0000 0000 0000 0000 0000 0100b

— or —

short s = -4;
// short is a 16-bit format, so  
s = 1111 1111 1111 1100b

int i = s;
// conversion to 32-bit int, so  
i = 1111 1111 1111 1111 1111 1111 1111 1100b
Truncating Bit Representation

If we want to **reduce** the bit size of a number, C *truncates* the representation and discards the *more significant bits*.

```c
int x = 53191;
short sx = x;
int y = sx;
```

What happens here? Let's look at the bits in `x` (a 32-bit int), 53191:

```
0000 0000 0000 0000 1100 1111 1100 0111
```

When we cast `x` to a short, it only has 16-bits, and C *truncates* the number:

```
1100 1111 1100 0111
```

This is -12345! And when we cast `sx` back an int, we sign-extend the number.

```
1111 1111 1111 1111 1100 1111 1100 0111  // still -12345
```
Truncating Bit Representation

If we want to **reduce** the bit size of a number, C **truncates** the representation and discards the *more significant bits*.

```
int x = -3;
short sx = x;
int y = sx;
```

What happens here? Let's look at the bits in `x` (a 32-bit int), -3:

```
1111 1111 1111 1111 1111 1111 1111 1101
```

When we cast `x` to a short, it only has 16-bits, and C **truncates** the number:

```
1111 1111 1111 1101
```

This is -3! If the number does fit, it will convert fine. `y` looks like this:

```
1111 1111 1111 1111 1111 1111 1111 1101 // still -3
```
Truncating Bit Representation

If we want to reduce the bit size of a number, C truncates the representation and discards the more significant bits.

```c
unsigned int x = 128000;
unsigned short sx = x;
unsigned int y = sx;
```

What happens here? Let's look at the bits in x (a 32-bit unsigned int), 128000:

```
0000 0000 0000 0001 1111 0100 0000 0000
```

When we cast x to a short, it only has 16-bits, and C truncates the number:

```
1111 0100 0000 0000
```

This is 62464! **Unsigned numbers can lose info too.** Here is what y looks like:

```
0000 0000 0000 0000 1111 0100 0000 0000  // still 62464
```
The `sizeof` Operator

```c
long sizeof(type);
```

// Example
```c
long int_size_bytes = sizeof(int);  // 4
long short_size_bytes = sizeof(short);  // 2
long char_size_bytes = sizeof(char);  // 1
```

`sizeof` takes a variable type as a parameter and returns the size of that type, in bytes.
Recap

• Signed Integers
• Overflow
• Casting and Combining Types

Next time: How can we manipulate individual bits and bytes?